## **EAST Search History**

Ref#	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	8	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency)	USPAT	OR	OFF	2008/02/27
S2		circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 20:14

		same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency)				
SS	114	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27
S4	4	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27

S5 4 circuit\$6 and US-PGPUB; OR OFF 2008/02/27 digital same USPAT; FPRS; display\$6 and generat\$6 and DERWENT; IBM_TDB  pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and		same did same co or ADC) (sampl\$ clock\$6 \$6 and h same fre and corr same ph	nvert\$6 and 6 same and equal nigh\$6 equency) ect\$6				
(analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 same phase\$6 same differen\$6	55	digital sa display\$ generats plural\$6 pixel\$1 same im same da receive\$ associat (receiv\$ time same signand high frequence circuit\$6 (analog same digital same coor ADC) (sampl\$ clock\$6 \$6 and high same coor same phenomenant correspondent to the same free and correspondent to the same phenomenant to the same phenomenan	ame USP. 6 and EPO 66 and DER same IBM. same d (analog age\$6 ata and 66) and 66 and ne e\$6 gnal\$6) n\$6 same cy same d and and same to gital nivert\$6 and 6 same and equal nigh\$6 equency) ect\$6 nase\$6	AT; FPRS; ; JPO; WENT;	OR	OFF	

S6	2	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 same phase\$6 same differen\$6 and long\$6 same term\$6 same drift \$6	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27
S7	3	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to	USPAT	OR	OFF	2008/02/27 20:36

clock\$6 and ed \$6 and high\$6 same frequent and correct\$6 same phase\$6 same differens	cy)			
S8 7 circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (anal same image\$6 same data and receiv\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6 and high\$6 sa frequency sam circuit\$6 and (analog same same digital same convert\$0 or ADC) and (sampl\$6 same clock\$6 and ed\$6 and high\$6 same frequency and correct\$6 and phase\$6 same differents	log 6 6 6 6 7 8 8 8 8 8 8 8 8 8 8 9 9 9 9 9 9 9 9 9	OR	OFF	2008/02/27

59	5	circuit\$6 and digital same display\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock	USPAT	OR	OFF	2008/02/27 20:37
S10	3	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to	USPAT	OR	OFF	2008/02/27 20:38

		same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6) and feedback\$6				
S11	2	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6) and feedback\$6 and drift\$6	USPAT	OR	OFF	2008/02/27

S12	5	circuit\$6 and digital adj display \$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive \$6) and associat \$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or	USPAT	OR	OFF	2008/02/27 20:45
S13	4	phase same lock \$6 same loop\$6)  circuit\$6 and digital adj display \$6 and generat\$6 same plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive \$6) and associat \$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6	USPAT	OR	OFF	2008/02/27 20:46

	and the state of the	or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)				
S14	3	circuit\$6 and digital adj display \$6 and generat\$6 same plural\$6 same pixel\$1 same data and (analog same image\$6 same data same receive \$6) and associat \$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)	USPAT	OR	OFF	2008/02/27

S15	3		US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 21:11
S16		s6 same loop\$6) circuit\$6 and digital adj display \$6 and generat\$6 same plural\$6 same pixel\$1 same data and (analog same image\$6 same data same receive \$6) and associat \$6 and (receiv\$6 same time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6	USPAT	OR	OFF	2008/02/27 21:12

	ининининининининининининининининининин	or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)				
S17	2	circuit\$6 and digital adj display \$6 and generat\$6 same plural\$6 same pixel\$1 same data and (analog same image\$6 same data same receive \$6) and associat \$6 and (receiv\$6 same time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27
S18	0	"11408528" and "11408669"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	OR	OFF	2008/02/27 21:14

S19	0	"11408528"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 21:14
S20	0	"11408528"	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 21:15

2/28/2008 8:48:34 AM

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